

HESSI IDPU COMPREHENSIVE PERFORMANCE TEST

HSI_IDPU_CPT_D
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DAVE CURTIS

1. INTRODUCTION

1.1 Purpose

This document establishes the HESSI IDPU Comprehensive Performance Test to be performed at the stand-alone IDPU. This is a limited version to be run with simulators in the place of the Imager and Spectrometer.

1.2 Scope

This procedure will be performed to verify IDPU functionality during or following exposure of the IDPU to qualification or transportation environments.

2. SETUP

- a. Connect the IDPU, IPC, CPC together with the ETU harness (IDPU-J2, IPC-J2, IPC-J3, CPC-J2)
- b. Connect the Particle Detector to the IDPU using the ETU harness (IDPU-J5 to PD-J1)
- c. Connect the Particle Detector to the IPC bias supply with an ETU reynolds HV cable (PD-J2 to IPC-J6)
- d. Connect the 9 High Voltage supplies (IPC-HV1 to IPC-HV9) to the HV Load GSE box via ETU Reynolds HV cables.
- e. Connect the IDPU Detector inputs (IDPU-J11-J19) to the Spectrometer load simulator, with signal inputs to a Berkeley Nucleonics PB-4 Pulser.
- f. Connect the Power Controller outputs (IDPU-J3 and IDPU-J4) to the Spectrometer/Imager load simulator ("Iggy box").
- g. Connect the ADP signals (IDPU-J7 and IDPU-J8) to the RAS/SAS simulators with the test harness.
- h. Connect the CPC power output (CPC-J3) to the cryocooler load simulator (white box connected to large resistors).
- i. Connect the IDPU signal harness (IDPU-J1) to the GSE cart (silver box).
- j. Connect the IPC power input (IPC-J1) to the GSE cart (switch box, OUT2)
- k. Connect the CPC power input (CPC-J1) to the GSE cart (switch box, OUT1)
- 1. Connect the PMT-RAS LED to the SR pulse generator via the Heathkit resistance box.
- m. Power on the GSE cart and boot up the computers
- n. Switch on the GSE interface box (silver box)
- o. Bring up the GSE programs, scgse50 and ssrk. On ssrk, set the buffer size to 64K and the packet size to packets, and push AcquireRT. On scgse display POWER.srd
- p. Install the HV and Actuator enable plugs in the IPC
- q. Power up the Spectrometer/Imager load simulator box. Set switch to COLD.

3. POWER-UP PROCEEDURE

a.	Re	cord the temperatures on the scgse received data display:	IDPU °C_	
			IPC °C_	
			CPC °C_	
b. c. d.	Sta Sta	rt SC_CPCON. Verify CPC current < 0.15A rt IDPU_INIT. Verify instrument mode = NORMAL, Errors=0	HP#1 Current _ HP #2 Current_),Voltages	
0		ninal rn on the Imager		_OK
e.				
	1.	Display IMGR.srd		
	2.	Start the IMGR_ON procedure. Verify that 5 IADP_PWR fies SOH ADP page are all "ON" (11111) and Imager voltages are +12, +14 all +/-0.3V) except P5D2V		_OK
	3.	Send "/IADPMODE 15" to clear any ADP errors. Verify that ADP_STATUS fields on the SOH ADP page read "OK"	the 4	OK
	4.	Load the default parameter file: start "v4s0r0_003". Verify al IADP_STATUS values on the SOH ADP page are "OK" (0)	14	_OK
	5.	Send /IADPRUN. Verify all 4 IADP_STATUS values on the page are "OK" (0), Simulator LEDs flash, and IADP_RASFFIADP_SASFRMS on the SOH_IADP_MD display count		_OK
	6.	Start the IADP_TM_ON procedure. Verify RAS and SAS im on the SSRGSE	age telemetry	_OK
	7.	Verify instrument current <1.0A	HP#1 Current _	
f.	Tu	rn on the Spectrometer Interface		
	1.	Display POWER.srd		
	2.	Start the IDIB_ON procedure. Verify that the voltages (+12, for all 9 DIBs are nominal (+/-0.3V)	+5, -5, -12)	_OK
	3.	Start the IDIB_TM_ON procedure. Verify Monitor Rates pacton the SSRGSE every 10 seconds	ckets appear	_OK
	4.	Verify instrument current <1.9A	HP#1 Current _	
g.	Pov	wer on the Particle Detector and PMT		
	1.	Enable the PMT High Voltage by sending "/IDPUARM 9". V	Verify that	OK

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2.	page reads 28V +/- 0.5V	ESOH Voltages IDPU_28HV_	
3.	Turn on the PMT: Start "IPMT_ON". Verify that IPMT_HV SOH PMT page starts to increment once a second	VDAC on the	_OK
4.	Record the Particle Detector temperature IPDT (bias will no IPDT>25C)	t be applied if IPDT_	
5.	With PD bias off, record the PD rates		
	IPDCTRA	IPDCTRB	
6.	Turn on the particle detector using the IPD_ON procedure. IPDHVDAC value on the SOH Particle Detector page starts once a second.	•	_OK
7.	Wait for IPDHVDAC and IPMT_HVDAC to reach their fine (IPDHVDAC = 122, IPMT_HVDAC = 130)	al values.	_OK
8.	Verify instrument current <2.0A	HP#1 Current _	

4. TEST PROCEDURE

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- a. Display PMT.srd
- b. Send /IDPUDUMPTBLB
- c. With the Stanford Research waveform generator (SR) connected to the RAS
 PMT dust cover power off, read the PMT count rate off the SOH Memory
 Dump page, value #1 (should be <5)
 Value:
- d. Turn on Set the SR. Verify the following settings (should be recalled on power-up):
 - 1. Amplitude = 5Vpp
 - 2. Offset = 2.5Vpp
 - 3. Modulation=Off
 - 4. Waveform = ARB
 - 5. Frequency = 1099.9Hz
- e. Set the Heathkit resistance box connected to the SR to 100Kohms (HI range).
- f. Record the PMT count rate, value #1 in the Memory Dump page. Should be 61-6f hex. Value: _____
- g. Set the SR to Square waveform, Frequency=100Hz (should change frequency automatically when you change waveform select).
- h. Record the PMT count rate, value #1 in the Memory Dump page. Should be about 71 hex. Value: _____
- i. Set the resistance box to 470K
- j. Record the PMT count rate, value #1 in the Memory Dump page. Should be about 47 hex. Value:
- k. Set the resistance divider back to 100K
- 1. Set the SR back to ARB waveform

4.2	Pa	rticle De	etector T	est							
	a.	Displa	ay PD.srd								
	b.	Verify	nominal no	PD settin	ngs on Pa	rticle Det	ector disp	olay: IPD	THRESH	I=6,	
		IPDH	VDAC=1	22							OK
	c.	Send '	'/IPDTHI	RESH 2".	Record	PD rates					
							IPDCTI	RA	IPD	CTRB	
	d.	Send '	'/IPDTHI	RESH 4".	Record	PD rates					
							IPDCTI	RA	IPD	CTRB	
	e.	Send '	'/IPDTHI	RESH 6".	Record	PD rates					
							IPDCTI	RA	IPD	CTRB	
4.3	H	V Test									
	a.	Displa	ay SPEC.	srd							
	b.	•	-		-	on SPEC	-	•			OK
	c.	If HV	28 is not	on, type "	'/IDPUAI	RM 1". \	erify SPl	HV is En	abled, an	d	
		-	_28HV is								OK
	d.	Type	"IHV_RA	MP3700	", then se	nd "/IHV	PERIOD	1". Wai	t for DA	C values	
		of 190)								OK
	e.	Recor	d voltage	s at BOB	Pins 1-9	.v.pin 15	below. V	erify app	x 3700m	.V	OK
HV	Ou	ıtputs									-
		1	2	3	4	5	6	7	8	9	

k.

4.4 Cryocooler Test Verify that the CPC Status on the SOH Spectrometer Power page is "TRIPPED" TC Verify:_ Start the script "ICRYO_ON". Verify that CPC Status is now "OK" b. TC Verify: Send the command "/ICRYOMAIN 150". Record the value of HP#2 Peak c. Current HP#2 Max Current: On the Cryocooler load simulator, verify that the main current waveform is a d. smooth sinusoid at about 60Hz TC Verify:_ On the Cryocooler load simulator, record the RMS current and voltage (should e. be about 9V/9A) Cryomain RMS Volts:_____ Cryomain RMS Current:_____ Send the command "/ICRYOPHASE 32". Verify that ICRYOPHASE on the f. SOH Spectrometer Power page reads 32 TC Verify: Send the command "/ICRYOBAL 100". Verify that ICRYOBAL on the SOH g. Spectrometer Power page reads 100 TC Verify: On the Cryocooler load simulator, verify that the balance current waveform is a h. smooth sinusoid at about 60Hz TC Verify: i. On the Cryocooler load simulator, record the RMS voltage CryoBalance RMS Volts: Send the command "/ICRYOBAL 0". Verify that ICRYOBAL on the SOH j. Spectrometer Power page reads 0 TC Verify:

Send the command "/ICRYOMAIN 0". Record the value of HP#2 Current

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HP#2 Current:

4.5 Cold Plate Heater

a.	Set the HP current limit up: "Set HPA1=6"
b.	Record the following values from the SOH Spec power page:
	ICP1T
	ICP2T
	ICT1T
	ICT2T
	ITST
c.	Start the procedure "ISPEC_CPHTR_ON". Record the following values off the
	SOH Spec Power page:
	ICPPWR
	IDPU_P100V
	ICPHTR1
	ICPHTR2
	ICPHTR3
	HP#1 Current
d.	Verify that the CP heater LEDs come on on the load simulator GSE
	TC Verify
e.	Start the procedure "ISPEC_CPHTR_OFF". Verify that IDPU_P100V is set to
	zero volts (+/- 5V). TC Verify

f. Send the command "/IDPUNORMAL". Verify that the IDPU_MODE on the

SOH Executive page reads "NORMAL"

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TC Verify:_____

4.6 Detector Interface

- a. Connect the BN pulser to the Spectrometer Event Simulator input. Set amplitude to 900 00 x 1000 attenuation (all attenuator switches up), positive pulse, about 270Hz, 0.2us rise time, 200us fall time, square-top pulse with maximum width adjustment.
- b. On SSRGSE, select the "Spectra" and "Monitor rates" displays. Arrange the displays on the screen to show all three plots. On the Spectra display select detector 1, Log counts.
- c. Verify that all 9 detectors are making events at about the same rate (about 272 counts) on front and rear fast and slow counters. Verify that all 9 detectors have 99% live time front and rear:

 TC Verify:_____
- d. Verify on the Spectra plot that there are events in a single bin for front and rear (FWHM about 1 for Front, about 3 for rear). Record the central bin number in the table below.
- e. Select the next detector on the Spectra display and repeat step d for each detector.

Gain	Table

Bin	1	2	3	4	5	6	7	8	9
Front									
Rear									

f. Decrease the amplitude of the BN pulser until the Slow rates in each detector are decreased by 50%. Record the pulser amplitude for each segment in the table below

Slow Threshold Table

Ampl	1	2	3	4	5	6	7	8	9
Front									
Rear									

g. Set the BN pulser to about 120 00 x 10 (only the last attenuation switch up). For each detector rear segment, adjust the pulser amplitude until the counts are half in the low gain and half in the high gain spectra on the SSR GSE. Record the amplitude setting below.

Crossover Pulser Amplitude

Ampl.	1	2	3	4	5	6	7	8	9
Rear									

4.7 Spectrometer Attenuator Actuators

a.	Display ATTEN.srd	
b.	Set to engineering mode. Send the command "/IDPUENGIN". IDPU_MODE on the SOH Executive page shows "Engineering mode."	•
		TC Verify:
c.	Set the Load Simulator GSE switches to LOCKED, Valve CLO OUT2, status=RELAXED. Verify that this status is reflected i display.	•
d.	Toggle the status switches on the load simulator GSE to TENS this is reflected on the SOH display. Then toggle the switches RELAXED.	•
e.	Start the procedure "iatt_lockdown". Verify that the LOCKDO	OWN LED lights.TC Verify:
f.	Toggle the LOCKDOWN status switch on the load simulator CUNLOCKED. Verify that the shutter LOCK status reads "UN	
		TC Verify:
g.	Start the procedure "iatt_out1". Verify that the OUT1 LED lig simulator GSE. Then move the status switch to OUT1. Verify SHUTTER1 status reads "OUT"	
h.	Start the procedure "iatt_in1". Verify that the IN1 LED lights of simulator GSE. Then move the status switch to IN1. Verify the SHUTTER1 status reads "IN"	
i.	Start the procedure "iatt_unstick1". Verify that the UNSTICK the load simulator GSE	1 LED lights on TC Verify:
j.	Start the procedure "iatt_in2". Verify that the IN2 LED lights of simulator GSE. Then move the status switch to IN2. Verify the SHUTTER2 status reads "IN"	
k.	Start the procedure "iatt_out2". Verify that the OUT2 LED light simulator GSE. Then move the status switch to OUT2. Verify SHUTTER2 status reads "OUT"	
1.	Start the procedure "iatt_unstick2". Verify that the UNSTICK2 the load simulator GSE	LED lights on TC Verify:
m.	Start the procedure "ispec_valve". Verify that the valve LED of simulator GSE lights.	on the load TC Verify:
n.	Toggle the status switch for the valve to OPEN. Verify that the display reads OPEN.	e switch status on TC Verify:
0.	Start the procedure "ispec_valveb". Verify that the backup val load simulator GSE lights.	ve LED on the TC Verify:

4.8 RAS Shutter

If not engineering mode, send the command "/IDPUENGIN". Verify that the IDPU_MODE on the SOH Executive page shows "Engineering"
TC Verify:
Start the procedure "iras_openshutter". In the course of the procedure, the monitor the following:
On the Actuator page, IDPU_ACTV goes to ≈10V. IDPU_ACTV:
■ HP#2 Current goes to ≈1A HP#2 Current:
 RAS shutter LED on load simulator lights Engineer Verify:
Start the procedure "iras_openshutterb". In the course of the procedure, the monitor the following:
 On the Actuator page, IDPU_ACTV goes to ≈10V. IDPU_ACTV:
■ HP#2 Current goes to ≈1A HP#2 Current:
 RAS backup shutter LED on load simulator lights Engineer Verify:
Send the command "/IDPUNORMAL". Verify that the IDPU_MODE on the SOH Executive page reads "NORMAL" TC Verify:

4.9 Imager Functional

a.	displays (SAS1, SAS2, SAS2, RAS)	OK
b.	Stop the ADP by sending "/IADPSTOP". Verify LEDs on simulators stop flashing.	OK
c.	Push the "Save to File" button on SSRJ.	
d.	Re-start the ADP by sending "/IADPRUN". Verify LEDs on simulators start flashing again.	OK
e.	Wait 60 seconds, then send stop recording by pushing the "Save to File" button again	n OK
f.	On SSRJ, push the Replay File button and select the most recent file (just collected). File Name:	
σ.	Verify the data in the file contains PF data and nominal images.	OK

4.10 Imager Heaters

a.	Display Thermal/.srd	TC Verify
b.	Start "IMGR_TEST_HTRS". Verify:	
	UGTPWM =Primary	
	■ LGTPWM = Primary	
	■ IRASPWM = Enabled	TC Verify
c.	Verify that, after a few minutes, the IRASHTRV, IUGTHTRPV, and ILG read 28V periodically	THTRPV TC Verify
d.	Verify that the primary UGT and LGT, and the RAS heater LEDs are flash load simulator GSE.	hing on the TC Verify
e.	The command script will automatically shift to the backup heaters. Verify:	
	UGTPWM =Backup	
	■ LGTPWM = Backup	
	■ IRASPWM = Enabled	TC Verify
f.	Verify that the backup UGT and LGT, and the RAS heater LEDs are flash load simulator GSE.	ing on the TC Verify
g.	The system will automatically turn off the heaters. Verify that UGT, LGT heater LEDs stop flashing.	T, and RAS TC Verify